I, Pare K. Trzyna (Reg. No. 32, 601), hereby certify that this entry a TRANSE or fee is being sent via Federal Express to the Examiner on the date indicated below and is addressed to the Commissioner of Patents and Trademarks, Waehington, D.C. 20231 on the date set term below:

PATENT

Paper No. 4

File: Graff-P1-01

Peter K. Trzyna (Reg. No. 32,601)

Date: ____August 8, 2001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors

Richard A. Graff

Serial No.

09/785,254

Filed

February 16, 2001

For

FURTHER IMPROVED SYSTEM AND METHODS

FOR COMPUTING TO SUPPORT DECOMPOSING

PROPERTY INTO SEPARATELY VALUED

COMPONENTS

Group Art Unit

2165

Examiner

:

Honorable Commissioner of Patents and Trademarks Washington, D.C. 20231

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AUG 1 7 2001

TRANSMITTAL LETTER

Technology Center 2100

SIR:

Transmitted herewith for filing in the above-identified patent application are the following:

- 1. Preliminary Amendment; and
- 2. Amended Version of the Claims.

The Commissioner is hereby authorized to charge any fees associated with the above-identified patent application or credit any overcharges to Deposit Account No. 50-0235.

Please direct all correspondence to the undersigned at the address given

below.

Date: August 8, 2001

P.O. Box 7131 Chicago, IL 60680-7131 (312) 240-0824

Respectfully submitted,

Peter K. Trzyner (Reg. No. 32,601)

I hereby certify this correspondence is being filed by depositing it who he United States Postal Service as first envelope with sufficient postage and addressed to the Commissioner of Patents and Trademarks, Washington,

PATENT

Paper No. 4

File: Graff-P1-01

<u> August 8, 2001</u> Date:

D.C. 20231 on the date indicated below.

Signed:

Peter K. Trzyna (Reg.

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Honorable Commissioner of Patents and Trademarks Washington, D.C. 20231

> PRELIMINARY AMENDMENT RECEIVED

11/29/2001 TDADER : 00000001 500235

AUG 1 7 2001

01 FC:203 02 FC:202 324AMENDMENT 160.00 CH

Technology Center 2100

Please enter the amendment set forth below:

In the Claims Α.

Please amend claims 1-63 as set forth below.

A computer apparatus for changing digital electrical signals to separately value components temporally decomposed from property, the computer apparatus including: an input device operable for converting input data representing property into